

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Previously Presented) An apparatus comprising:
a power converter;
said power converter including a charge pump capacitor, said charge pump capacitor coupled to a two transistor totem-pole configuration in said converter so as to drive a primary of an isolation transformer; and
wherein a parasitic diode in one transistor opposes a parasitic diode in the other transistor.
2. (Original) The apparatus of claim 1, wherein said charge pump capacitor is coupled to drive the primary of an isolation transformer without signal rectification at least in part by being adapted to switch between charging and discharging operation at different portions of a current cycle.
3. (Original) The apparatus of 2, wherein said charge pump capacitor is further adapted to switch between charging and discharging operation at or substantially near zero current.
4. (Original) The apparatus of claim 1, wherein said power converter is incorporated on a motherboard.
5. (Original) The apparatus of claim 1, wherein said power converter is coupled to a DC power consuming device.

6. (Original) The apparatus of claim 5, wherein said DC power consuming device comprises at least one of a fax, printer, scanner, and copier.
7. (Original) The apparatus of claim 1, wherein said power converter comprises an AC-DC power converter.
8. (Original) The apparatus of claim 7, wherein said power converter includes an input pi filter.
9. (Original) The apparatus of claim 7, wherein a secondary of said isolation transformer is coupled in a circuit to perform full-wave rectification.
10. (Original) The apparatus of claim 1, wherein said primary of said isolation transformer is coupled so as to resonate during operation.
11. (Previously Presented) A circuit comprising:
a power converter;
said power converter comprising at least two transistor totem-pole configurations; one of said transistor totem-pole configurations coupled to an AC line and another of said transistor totem-pole configurations coupled to an AC neutral; wherein a parasitic diode in one transistor opposes a parasitic diode in the other transistor; and a pump capacitance device coupled between said transistor totem-pole configurations to drive a primary of an isolation transformer.
12. (Original) The circuit of claim 11, wherein said isolation transformer is coupled in said power converter to form a series fed, resonant, isolation transformer.

13. (Previously Presented) The circuit of claim 11, wherein said transistor totem-pole configurations are coupled in said converter so to yield a power transfer that is substantially a linear function of switching frequency.
14. (Previously Presented) The circuit of claim 11, wherein said transistor totem-pole configurations are coupled in said converter so as to turn at least some transistors of said configurations on at substantially zero current.
15. (Previously Presented) The circuit of claim 14, wherein said transistor totem-pole configurations are coupled in said converter so as to turn at least some of said transistors of said configurations off at substantially zero current.
16. (Original) The circuit of claim 15, wherein said transistor configurations are coupled in said converter so as to turn all of said transistors of said configurations on and/or off at substantially zero current.
17. (Previously Presented) The circuit of claim 11, wherein at least one transistor comprises a MOSFET.
18. (Original) The circuit of claim 11, wherein said pump capacitive device comprises a capacitor.
19. (Original) The circuit of claim 11, wherein said power converter is incorporated into a motherboard.
20. (Previously Presented) The circuit of claim 11, wherein said power converter is coupled to a DC power consuming device.
21. (Original) The circuit of claim 20, wherein said DC power consuming device comprises at least one of a fax, printer, scanner, and copier.

22. (Original) The circuit of claim 11, wherein said power converter comprises an AC-DC converter.

23. (Original) The circuit of claim 11, wherein said power converter includes an input pi filter.

24. (Original) The circuit of claim 11, wherein a secondary of said power converter is coupled so as to provide full wave rectification.

25. (Currently Amended) A method of converting power comprising:
charging an electrical storage element during a portion of a cycle so that current is provided by said electrical storage element during another portion of the cycle without rectification;

wherein said electrical storage element is a charge pump capacitor;
wherein the electrical storage element is coupled to a two transistor totem-pole configuration; and
wherein a parasitic diode in one transistor opposes a parasitic diode in the other transistor.

26. (Original) The method of claim 25, wherein said electrical storage element is charged during resonant operation of a primary circuit of an isolation transformer.

27. (Original) The method of claim 25, wherein charging and discharging of said electrical storage element switches at substantially zero current.

28 (Original) The method of claim 27, wherein transistor configurations are employed to accomplish the switching.

29. (Original) The method of claim 28, wherein said transistor configurations include MOSFETs arranged in a totem pole configuration.

30. (Original) The method of claim 28, wherein power is converted as substantially a linear function of switching frequency.

31. (Cancelled)

32. (Original) The method of claim 25, wherein feedback is employed for synchronization between an applied input voltage signal and an output voltage signal.

33. (Original) The method of claim 25, wherein converting power comprises AC to DC conversion.

34. (Previously Presented) A system comprising:
a DC power consuming device and an AC-DC power converter;
said power converter including a charge pump capacitor, said charge pump capacitor coupled to a two transistor totem-pole configuration in said converter so as to drive a primary of an isolation transformer; and
wherein a parasitic diode in one transistor opposes a parasitic diode in the other transistor.

35. (Original) The system of claim 34, wherein said charge pump capacitor is coupled to drive the primary of an isolation transformer without signal rectification at least in part by being adapted to switch between charging and discharging operation at different portions of a current cycle.

36. (Original) The system of 35, wherein said charge pump capacitor is further adapted to switch between charging and discharging operation at or substantially near zero current.
37. (Original) The system of claim 34, wherein said power converter is incorporated on a motherboard with said DC power consuming device.
38. (Original) The system of claim 34, wherein said DC power consuming device comprises at least one of a fax, a printer, a scanner and a copier.
39. (Canceled)
40. (Previously Presented) The system of claim 34, wherein said power converter includes an input pi filter.
41. (Previously Presented) The system of claim 34, wherein a secondary of said isolation transformer is coupled in a circuit to perform full-wave rectification.
42. (Original) The system of claim 34, wherein said primary of said isolation transformer is coupled so as to resonate during operation.
43. (Currently Amended) An apparatus comprising:
means for converting from an AC voltage to a DC voltage;
said means for converting including a means for isolation, said means for isolation including a primary and a secondary;
said means for converting being coupled so that, in operation, AC to DC voltage rectification does not occur on the primary of said means for isolation;
wherein said means for converting includes being coupled to a two transistor totem-pole configuration with a charge pump capacitor; and

wherein a parasitic diode in one transistor opposes a parasitic diode in the other transistor.

44. (Original) The apparatus of claim 43, wherein said means for converting is coupled to drive the primary of said means for isolation without signal rectification at least in part by being adapted to switch between charging and discharging operation at different portions of a current cycle.

45. (Original) The apparatus of 44, wherein said means for converting includes a charge pump capacitor, said capacitor being further adapted to switch between charging and discharging operation at or substantially near zero current.

46. (Original) The apparatus of claim 43, wherein said means for converting is incorporated on a motherboard.

47. (Original) The apparatus of claim 43, wherein said means for converting is coupled to a DC power consuming device.

48. (Original) The apparatus of claim 47, wherein said DC power consuming device comprises at least one of a fax, printer, scanner, and copier.

49. (Original) The apparatus of claim 43, wherein said means for converting comprises an AC-DC power converter.

50. (Original) The apparatus of claim 49, wherein said power converter includes an input pi filter.

51. (Original) The apparatus of claim 49, wherein the secondary of said means for isolation is coupled in a circuit to perform full-wave rectification.

52. (Original) The apparatus of claim 43, wherein said primary of said means for isolation is coupled so as to resonate during operation.